New achievement: TSP chip structure can run 1 million billion operations per second

Chips with the TSP structure of Startup Groq can perform up to 1 peta task per second, that is, 1,000,000,000,000,000 actions per second.

Startup Groq has just announced an amazing achievement, the chip using their TSP architecture can perform up to 1 peta task in a second, ie 1,000,000,000,000,000 tasks in a second. You may not know, Groq is the startup behind the Tensor Streaming Processor (TSP) technology - the chip structure is also a new way of performing calculations.

With the TSP chip structure, Groq was the first to reach this impressive figure.

Jonathan Ross, co-founder and CEO of Groq, said this was good news for both chip makers and customers.

Groq engineers put software optimization to the forefront, so Groq's TSP architecture is superior to current CPU and GPU structures, achieving both flexibility and flexibility in parallel. more actions.

It is important that all machine learning models (both old and new), can take advantage of the processing power of the TSP structure.

The purpose of designing this chip structure of Groq is to provide an ideal solution for machine learning processing process and related AI research fields.

Groq's chip structure manager Dennis Abts further emphasized that Groq's solution is also designed to perform the tasks of many other simple, high-performance industries. The TSP structure is suitable for all tasks related to data processing or calculation.

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